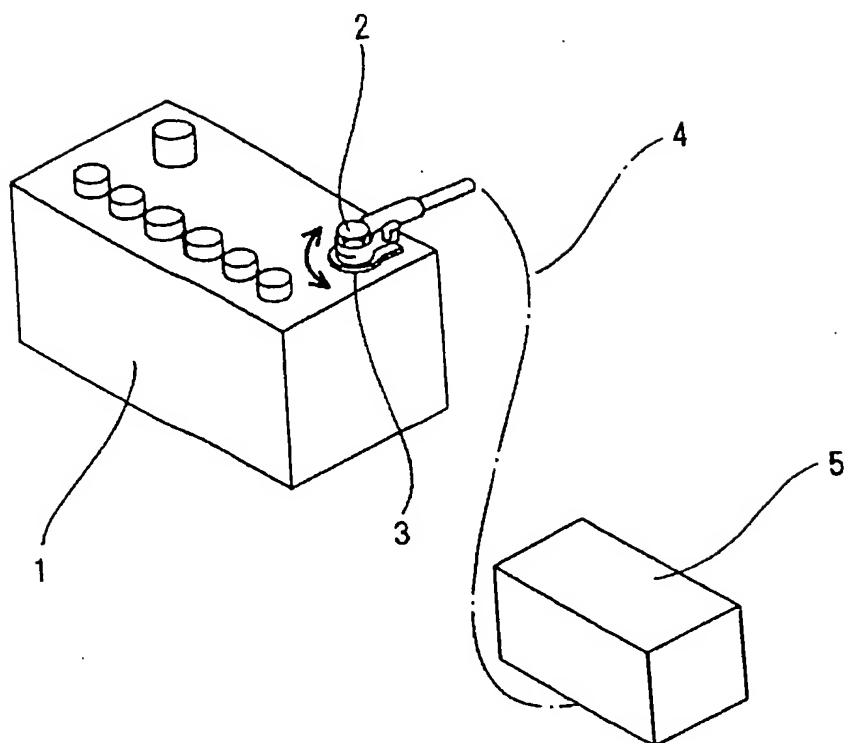


**Fig. 6**  
**RELATED ART**



**Fig. 7**  
**RELATED ART**

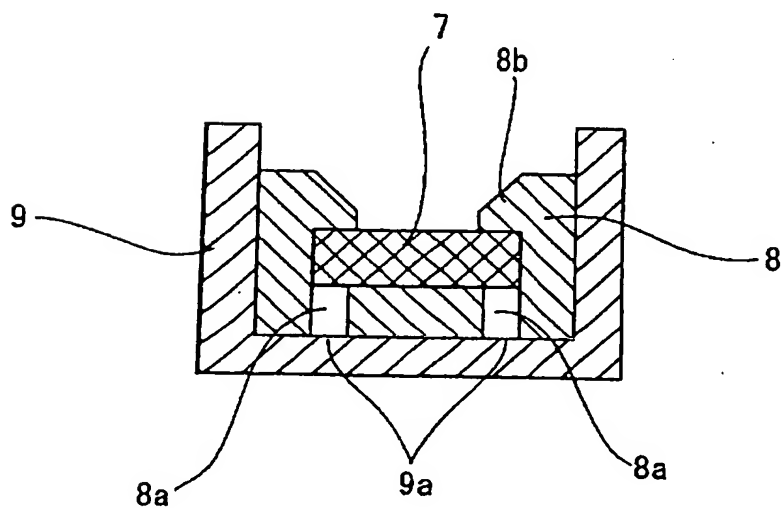
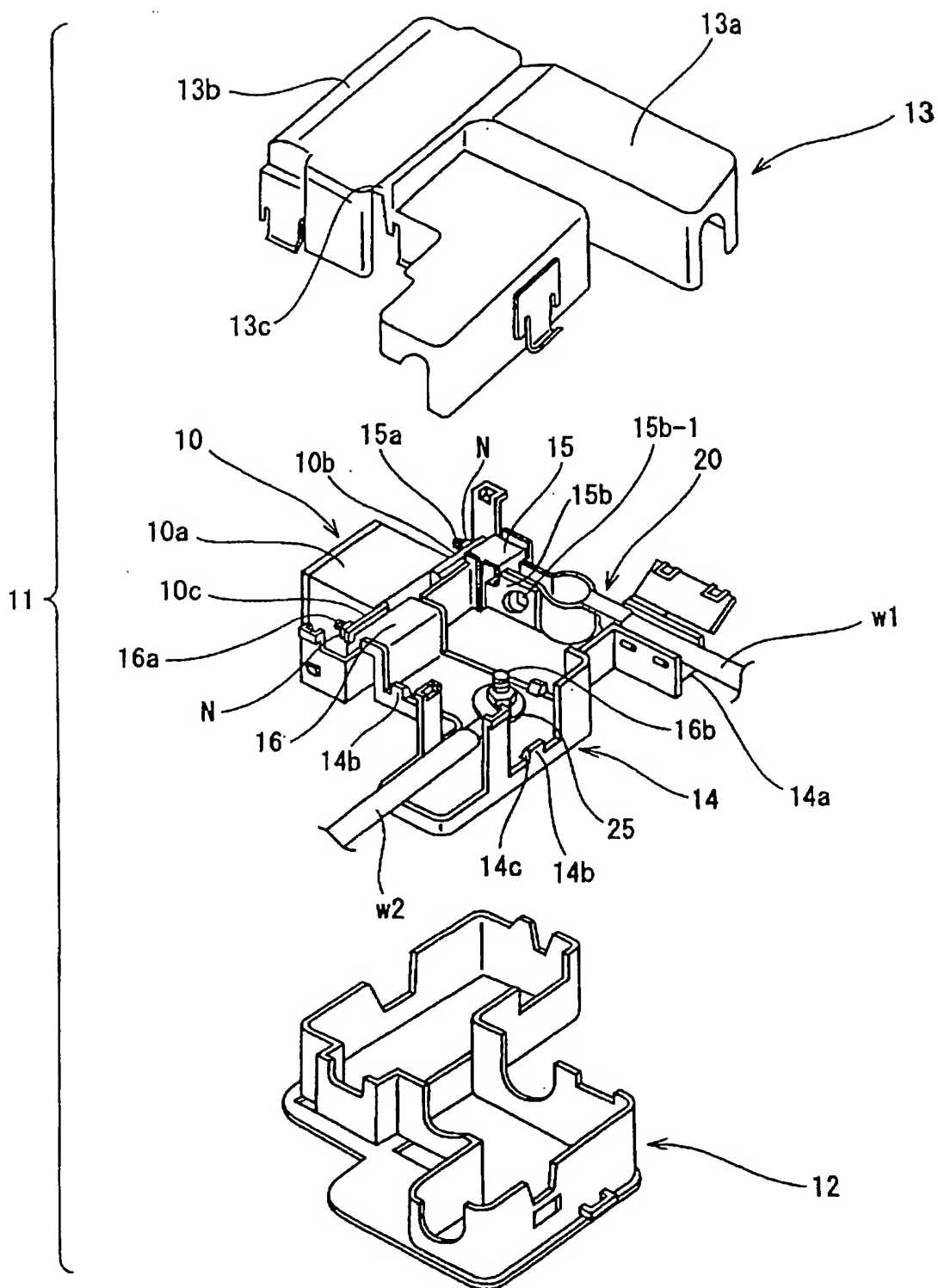
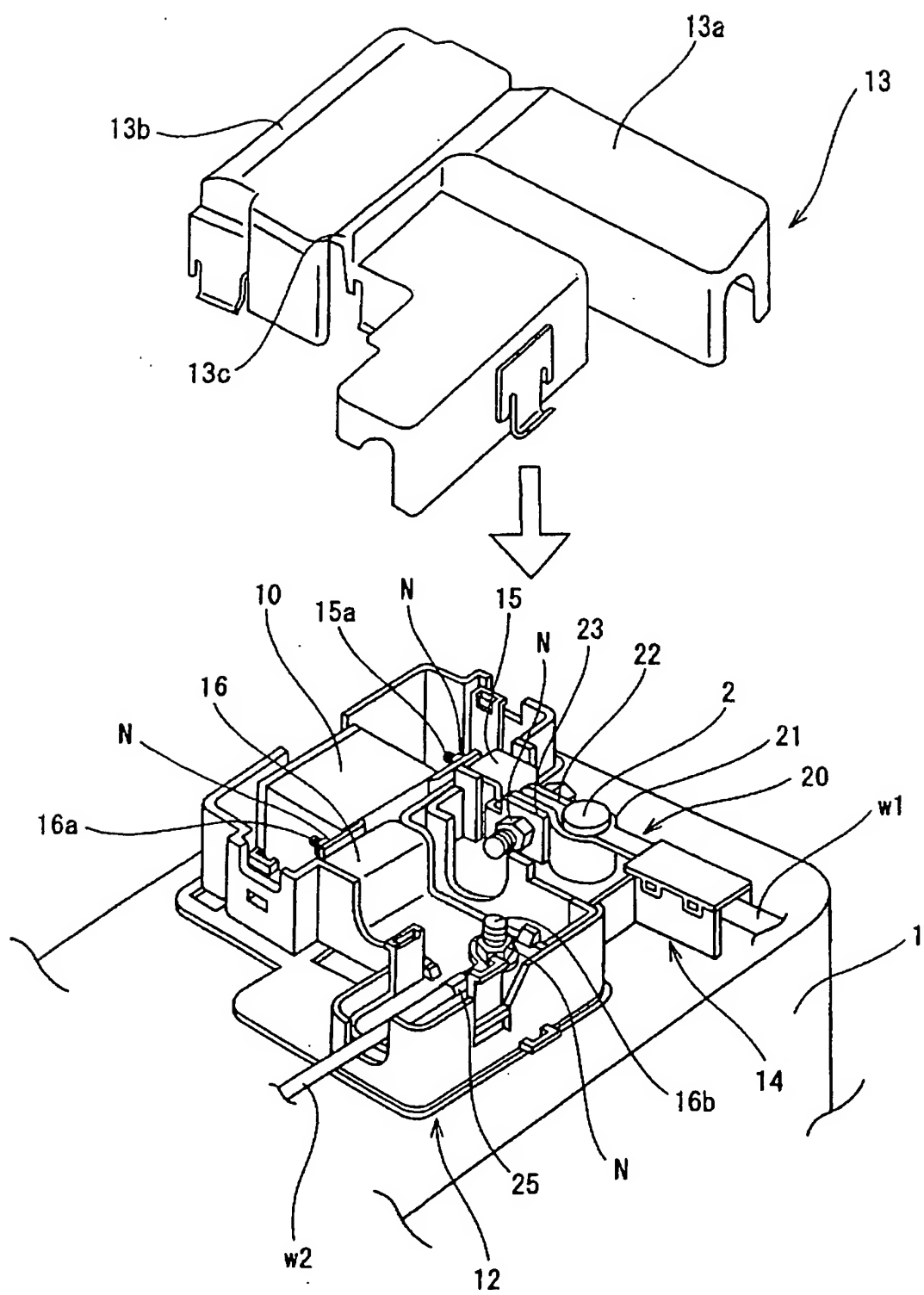


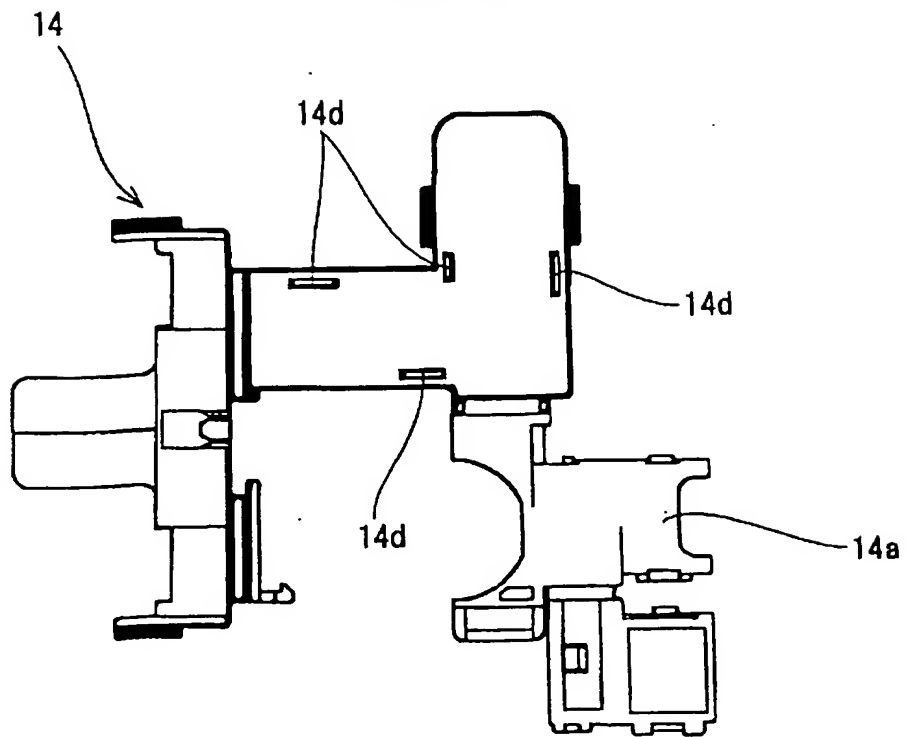
Fig. 1



**Fig. 2**



**Fig. 3A**



**Fig. 3B**

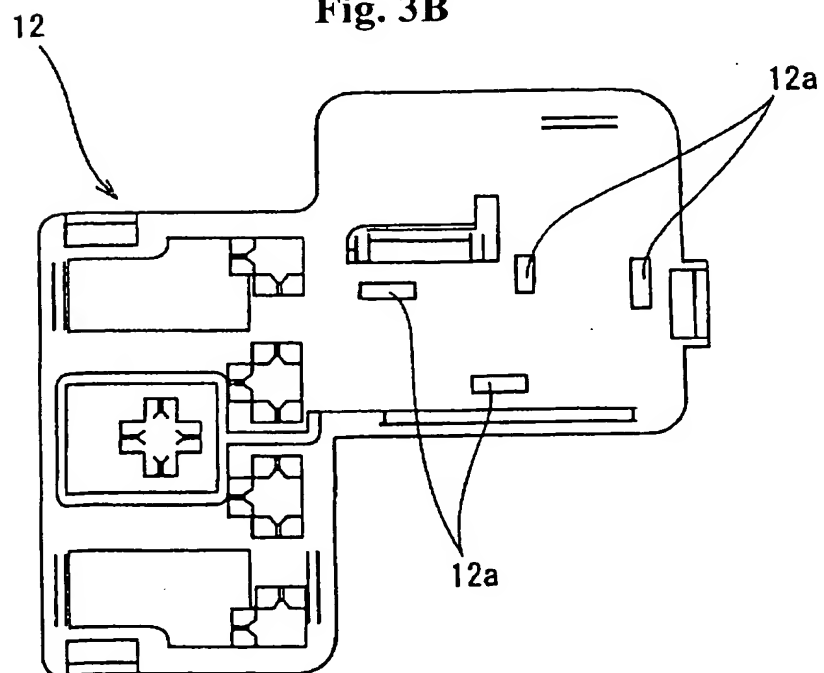


Fig. 1 is a cross-sectional view of a semiconductor device. It shows a substrate 12 with a base layer 12a. A central region 16 is covered by a cross-hatched layer 14c, which is flanked by regions 14b and 14d. The entire structure is surrounded by a thick layer 14.